

Application No. 09/743,997

Attorney Docket No.RCA-89130

REMARKS

Claims 1-9 are pending in this application with claims 1-7 and 9 being amended by this Response.

Claim 1-7 and 9 have been amended to clarify that the data formatted as OSD data is non-OSD control information. Support for this amendment can be found throughout the specification and specifically on page 4, line 9 to page 5, line 11.

Rejection of Claim 1 under 35 USC § 102(e)

Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by Sparks (U.S. Patent No. 6,034,738).

The present invention teaches a method of receiving non-OSD control information. The method includes receiving an analog video signal including non-OSD control information formatted as OSD data. The non-OSD control information formatted as OSD data is detected and extracted from the analog signal. The non-OSD control information is then processed to produce a control signal. The present claimed invention is concerned with insertion of control information into a video signal when the control information cannot be inserted according to conventional methods and in conventional locations within the signal. Claim 1 recites a method for extracting and processing the non-OSD control information for producing a control signal that has been formatted as OSD data and inserted into the video signal.

Sparks teaches a method for facilitating the on-screen display of recorder player status messages. Sparks returns a reproduced decoded, analog video signal to the digital recorder player for insertion of status messages. Recorder message data is coupled to the digital signal decoder for inclusion with the decoded analog video signal. A recorder command is coupled to the decoder to access recorder specific graphical messages. The recorder message data is tagged and coupled to the decoder to select recorder specific graphical messages if available or to display recorder

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 message data. Digital or analog operating mode determines recorder message display
 timing with respect to a reproduced decoded video signal.

Claim 1 has been amended to recite "A method of receiving non-OSD control information." Sparks teaches "a monitor display having three analog signal inputs; audio video signals and an audio and component video signal, for example, S-video or luminance and coloring components Y, Pr, Pb" (column 8, lines 3-6) and "The on-screen display message or OSD is coupled to an OSD inserter or adder, block 520 where the message is formatted for analog video display and added or combined with the MPEG decoded video signal" (column 2, lines 62-65). Sparks, however, neither discloses nor suggests "a method of receiving non-OSD control information" as claimed in claim 1 of the present invention. Sparks is concerned with a "Controller 115 may construct an on-screen display message or may read a predetermined stored message from memory block MEM," as stated in column 4, lines 59-61, while the present claimed invention is concerned with "receiving non-OSD control information" as claimed in claim 1 of the present invention. This "control information" includes such information as channel tuning instructions, not OSD messages.

Additionally, the applicant respectfully submits that Sparks' construction of an on-screen display message and reading a predetermined stored message from memory, as disclosed in column 4, lines 59-61, contrast the "extracting the detected non-OSD control information from the analog signal" and "processing the non-OSD control information for producing a control signal" as claimed in claim 1 of the present invention. Furthermore, the signal A/V+OSD 103 cited by the Examiner neither discloses nor suggests a video signal including non-OSD control information formatted as OSD data, as claimed in claim 1 of the present invention. Furthermore, although display 300 receives an A/V+OSD 103 as illustrated in Figure 2 of Sparks, display 300 neither discloses nor suggests "extracting the detected non-OSD control information from the analog signal; and processing the non-OSD control information for producing a control signal" as disclosed in claim 1 of the present invention. Thus, it is respectfully submitted that, in view of the above remarks, claim 1 of the invention

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 is not anticipated by Sparks. It is thus further respectfully submitted that this rejection
 is satisfied and should be withdrawn.

Rejection of Claims 1-3 and 5-8 under 35 USC § 102(e)

Claims 1-3, 5-8 are rejected under 35 U.S.C. 102(e) as being anticipated by
 Knox et al. (U.S. Patent No. 6,480,238).

Knox et al. teach an apparatus for generating an OSD message by constructing
 an OSD bitstream defining a single field of OSD data. The OSD bitstream contains an
 OSD header and OSD data. An OSD unit retrieves pixel control information from the
 OSD header which is programmed by a processor of a decoding/displaying system.
 The OSD header contains information including various pointers that are used to
 provide instructions as to the treatment of the OSD data.

Unlike the present claimed invention, Knox et al. “performs real time audio
 and video decompression of various data streams (bitstreams)” as disclosed in column
 2, lines 51-53. Knox et al. neither suggest nor disclose “A method of receiving non-
 OSD control information” as claimed in claim 1 of the present invention. Knox et al.
 are concerned with “an apparatus and concomitant method for generating OSD
 messages by constructing a valid OSD bitstream with instructions in the OSD header
 to repeat a top field of OSD data in the bottom field for an OSD region” as stated in
 column 1, lines 63-67, while the present claimed invention is concerned with
 “receiving non-OSD control information” as claimed in claim 1 of the present
 invention. This “control information” can include such information as channel tuning
 instructions, not OSD messages. Furthermore, Knox et al. neither disclose nor
 suggests “extracting the detected non-OSD control information from the analog signal;
 and processing the non-OSD control information for producing a control signal” as
 disclosed in claim 1 of the present invention.

Thus, it is respectfully submitted that, in view of the above remarks, claim 1 is
 not anticipated by Knox et al. As claims 2-3 and 5-8 are dependent on claim 1 it is

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 respectfully submitted that these claims are not anticipated for the same reasons as
 claim 1. It is thus further respectfully submitted that this rejection is satisfied and
 should be withdrawn.

Rejection of Claims under 35 USC § 103(a)

Claims 4 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over
 Knox et al. (U.S. Patent No. 6,480,238).

Regarding claim 4, as discussed above, Knox et al, neither disclose nor suggest
 “the non-OSD control information is information usually included in a blanking
interval of an analog video” as claimed in claim 4 of the present claimed invention.
 Knox et al. teach that “the memory 140 is used to store a plurality of data including
 compressed data, decoded images and the OSD bit map,” as discussed in column 3,
 lines 27-29. This clearly demonstrates, as stated above, that Knox et al. are concerned
 with retrieving pixel control information from the OSD header which contains
 information including various pointers that are used to provide instructions as to the
 treatment of the OSD data. Knox et al, however, are not concerned with “non-OSD
 control information” which includes such information as channel tuning instructions
 as opposed to OSD messages.

The present invention as claimed in claim 9 recites a method of formatting
 non-OSD control information. The method includes receiving a digital video signal.
 Non-OSD control information signal is provided to an OSD generator and formatted
 as OSD data. The OSD data is then inserted into the video signal. The digital video
 signal is converted into an analog video signal the analog signal including the non-
 OSD control information signal formatted as OSD data is provided to an external
 device.

As discussed above, Knox et al. are concerned with generation of an OSD
 bitstream and insertion of the OSD bitstream into a video signal. Knox et al. do
 disclose forming an OSD packet including OSD header and OSD data. However,

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Knox et al. neither disclose nor suggest "formatting non-OSD control information as OSD data" and "inserting the OSD data into the video signal" as claimed in claim 9 of the present invention. Knox et al. teach that "When the OSD function is enabled for a particular image or frame, the processor 130 manipulates the data in memory 140 to construct an OSD bitstream. The OSD bitstream contains an OSD header and OSD data" (column 3, lines 62-67). Applicant further submits that the OSD unit in Knox neither disclose nor suggest "formatting non-OSD control information as OSD data". It is thus shown that Knox et al. is not concerned with "formatting non-OSD control information as OSD data" as claimed in claim 9 of the present invention.

Thus, it is respectfully submitted that, in view of the above remarks, the present invention as claimed in claim 9 is not anticipated by Knox et al. It is thus further respectfully submitted that this rejection is satisfied and should be withdrawn.

Having fully addressed the Examiner's rejections, it is believed that, in view of the preceding amendments and remarks, this application stands in condition for allowance. Accordingly then, reconsideration and allowance are respectfully solicited. If, however, the Examiner is of the opinion that such action cannot be taken, the Examiner is invited to contact the applicant's attorney at the phone number below, so that a mutually convenient date and time for a telephonic interview may be scheduled.

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No fee is believed due. However, if a fee is due, please charge the additional
fee to Deposit Account 07-0832.

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February 15, 2005